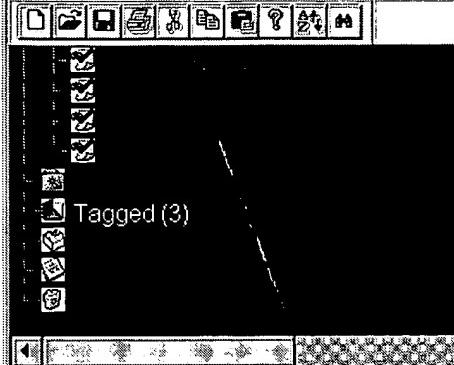


| L Number | Hits | Search Text  | DB    | Time stamp       |
|----------|------|--|-------|------------------|
| 7        | 1181 | 345/92   | USPAT | 2004/06/22 13:18 |
| 8        | 590  | 345/205  | USPAT | 2004/06/22 13:18 |
| 9        | 556  | 345/206  | USPAT | 2004/06/22 13:18 |
| 10       | 1830 | 345/92 or 345/205 or 345/206   | USPAT | 2004/06/22 13:50 |
| 11       | 1015 | (345/92 or 345/205 or 345/206) and (active adj matrix or active adj2 matrices)   | USPAT | 2004/06/22 13:53 |
| 12       | 91   | (345/92 or 345/205 or 345/206) and (active adj matrix or active adj2 matrices)same buffer\$1                           | USPAT | 2004/06/22 13:54 |
| 13       | 52   | (345/92 or 345/205 or 345/206) and (active adj matrix or active adj2 matrices)same buffer\$1 same line\$1              | USPAT | 2004/06/22 13:54 |
| 14       | 23   | (345/92 or 345/205 or 345/206) and (active adj matrix or active adj2 matrices)same buffer\$1 same line\$1 near5 signal | USPAT | 2004/06/22 13:57 |
| 15       | 52   | (345/92 or 345/205 or 345/206) and (active adj matrix or active adj2 matrices)same buffer\$1 same line\$1              | USPAT | 2004/06/22 13:57 |

**File View Edit Tools Window Help**

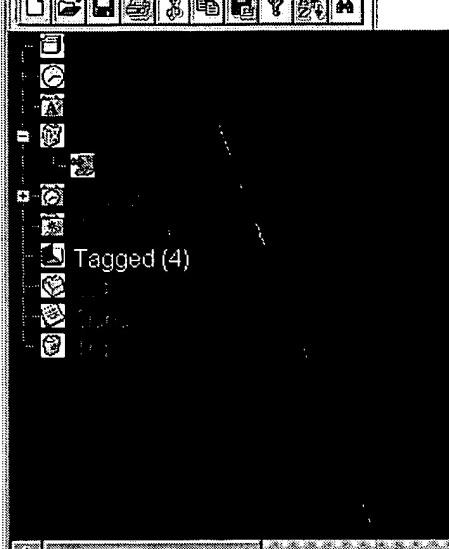
Search List Browse Queue Clear  
DBs USPAT  
Default operator: OR  Plural  Highlight all

BRD... ISR... Image Text HTML

|   | U                        | I                                   | Document ID    | Issue Date     | Pages    | Title  | Current OR | Current              |
|---|--------------------------|-------------------------------------|----------------|----------------|----------|--|------------|----------------------|
| 1 | <input type="checkbox"/> | <input checked="" type="checkbox"/> | US 20010011970 | 20010809       | 17       | DRIVE CIRCUIT OF ACTIVE MATRIX TYPE DISPLAY DEVICE AND                         | 345/30     |                      |
| 2 | <input type="checkbox"/> | <input checked="" type="checkbox"/> | A1             | US 20010045931 | 20011129 | Drive circuit of active matrix type display device having buffer with parallel | 345/92     |                      |
| 3 | <input type="checkbox"/> | <input checked="" type="checkbox"/> | US 6246387 B1  | 20010612       | 16       | Semiconductor display device   | 345/92     | 345/205;<br>345/206; |

Real...  
099...  
099...  
099...  
EAS...

1:10 PI  
EN



Search

DBs: USPAT  Plurals  
Default operator:  OR  AND  Highlight all

IS&R  Image  Text  HTML

|   | U                        | I                                   | Document ID       | Issue Date | Pages | Title  | Current OR | Current              |
|---|--------------------------|-------------------------------------|-------------------|------------|-------|--|------------|----------------------|
| 1 | <input type="checkbox"/> | <input checked="" type="checkbox"/> | US 20010011970 A1 | 20010809   | 17    | DRIVE CIRCUIT OF ACTIVE MATRIX TYPE DISPLAY DEVICE AND                         | 345/30     | 257/E21.7            |
| 2 | <input type="checkbox"/> | <input checked="" type="checkbox"/> | US 20010024185 A1 | 20010927   | 17    | Semiconductor display device   | 345/92     | 257/E27.1            |
| 3 | <input type="checkbox"/> | <input checked="" type="checkbox"/> | US 20010045931 A1 | 20011129   | 19    | Drive circuit of active matrix type display device having buffer with parallel | 345/92     | 257/E21.7            |
| 4 | <input type="checkbox"/> | <input checked="" type="checkbox"/> | US 6496171 B2     | 20021217   | 19    | Semiconductor display device   | 345/92     | 257/E27.1<br>345/100 |

